

## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### Department of Electrical & Electronics Engineering

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : **J GANESH PRASAD REDDY** 

2. Date of Birth : **29/11/1975** 

3. Unique ID

4. Education qualifications : **B.Tech, M.Tech,(Ph.D)** 

5. Work experience : **20 YEARS** 

5.1. Teaching : **20 YEARS** 

5.2. Research : ---

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **High Voltage Engineering with emphasis of** 

**POWER SYSTEMS** 

7. Courses taught at :

7.1. Diploma :

7.2. Post Diploma : --

7.3. Under Graduate : ECA, NT, CS, ACS, PS-I, II, SGP, PSOC, EDS, EM-I, BEEE,

S & S, ET, CS Lab, EM-I Lab, Sim Lab

7.4. Post Graduate : **HVDC, EDS, HVE, PSDS** 

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : **02** 

b) International

(Journals / Conferences) : 11





8.2. Master : **05** 

8.3. Ph. D

9. Projects carried out : **25** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : 02

13. No. of books published with details : **03 Network Theory, Fundamentals of electrical** 

**Engineering and Electrical Technology** 

14. No. of Workshops conducted / attended: Conducted 01, Attended 10



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### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : Dr. S B SHIVA KUMAR

2. Date of Birth : **08/07/1960** 

3. Unique ID

4. Education qualifications : **B.Tech, M.Tech, Ph.D** 

5. Work experience : **37 YEARS** 

5.1. Teaching : **37 YEARS** 

5.2. Research : ---

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **POWER SYSTEMS** 

7. Courses taught at :

7.1. Diploma :

7.2. Post Diploma : --

7.3. Under Graduate :

7.4. Post Graduate :

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in

a) National

(Journals / Conferences) : **32** 

b) International

(Journals / Conferences) : 10

8.2. Master : **15** 

8.3. Ph. D : **08** 





9. Projects carried out : **50** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : 10

13. No. of books published with details :



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### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : KIRAN KUMAR KUTHADI

2. Date of Birth : **10/07/1986** 

3. Unique ID

4. Education qualifications : **B.Tech, M.Tech,(Ph.D)** 

5. Work experience : **11 YEARS** 

5.1. Teaching : **11 YEARS** 

5.2. Research : ---

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **ELECTRICAL POWER SYSTEMS** 

7. Courses taught at :

7.1. Diploma :

7.2. Post Diploma : --

7.3. Under Graduate : PS-I, UEE, PE, PS-II, PSA, NNFL, ET, NA, SGP, PSOC, CS,

ECA-II,EC,EDS,LICA,EM-I LAB,EM-II LAB,N&ET LAB,

PE LAB, CS LAB, PS LAB

7.4. Post Graduate : **FACTS, PSDS, PSOC, EDS, PS LAB, APSP,** 

**PS & SIMULATION LAB** 

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : **02** 

b) International

(Journals / Conferences) : 19





8.2. Master : **05** 

8.3. Ph. D

9. Projects carried out : **15** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : **02** 

13. No. of books published with details : --



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### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : SRINIVASA RAO TADANKI

2. Date of Birth : **10/06/1978** 

3. Unique ID

4. Education qualifications : **B.Tech, M.Tech(Ph.D)** 

5. Work experience : **10 Years** 

5.1. Teaching : **10 Years** 

5.2. Research : ---

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **POWER SYSTEM ENGINEERING, MICRO GRIDS** 

7. Courses taught at :

7.1. Diploma : **ACM-I, II, DCM, ED, EM, EC, DC LAB, AC LAB** 

7.2. Post Diploma : --

7.3. Under Graduate : **EM-I, II, PS-I, II, SGP, ET, NT, PSOC, EMS, PE, CS,** 

EM-I LAB, EM LAB, EM-II LAB, EC LAB, PE LAB, CS LAB

7.4. Post Graduate : **SMART GRID, PSOC, MP&MC, RTCPS, PST** 

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) :

b) International

(Journals / Conferences) : **03** 

8.2. Master : **02** 





8.3. Ph. D : **01** 

9. Projects carried out : **25** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : **KOLLI NAGESWARA RAO** 

2. Date of Birth : **01/06/1986** 

3. Unique ID :

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **7 Years** 

5.1. Teaching : **7 Years** 

5.2. Research : -

5.3. Industry : -

5.4. Others : --

6. Area of Specialization : **POWER ELECTRONICS & ELECTRIC DRIVES** 

7. Courses taught at :

7.1. Diploma :

7.2. Post Diploma : --

7.3. Under Graduate : **BEEE, ECA-I, II, NA, PE, PE&SD, EDS, FACTS, EC LAB,** 

EM-I LAB, EM-II LAB, PE LAB

7.4. Post Graduate : RTCPS, FACTS, PSDS

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : **01** 

b) International

(Journals / Conferences) : 10

8.2. Master





8.3. Ph. D :

9. Projects carried out : **15** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : **GUTHIKONDATEJASWI** 

2. Date of Birth : **16/08/1989** 

3. Unique ID :

4. Education qualifications : **B.Tech, M.Tech(Ph.D)** 

5. Work experience : **6 Years, 4 Months** 

5.1. Teaching : **6 Years, 4 Months** 

5.2. Research : --

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **Power Electronics & Drives** 

7. Courses taught at :

7.1. Diploma : --

7.2. Post Diploma : --

7.3. Under Graduate : HVE, EMD, EM-I, RESS, EDS, PS-I, N&ET, BEE, SEM, UEE,

N & ET Lab, BEE Lab, E Sim Lab, EC Lab

7.4. Post Graduate : **PQ, PST, APSP, FACTS** 

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : **02** 

b) International

(Journals / Conferences) : 10

8.2. Master : **07** 



8.3. Ph. D : --

9. Projects carried out : 12

10. Patents : --

11. Technology Transfer : --

12. Research Publications : **02** 

13. No. of books published with details :



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### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : NALLIBOINA NAGESWARA RAO

2. Date of Birth : **06-08-1988** 

3. Unique ID

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **6 YEARS** 

5.1. Teaching : **6 YEARS** 

5.2. Research : ---

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **POWER ELECTRONICS & DRIVES** 

7. Courses taught at :

7.1. Diploma : --

7.2. Post Diploma : --

7.3. Under Graduate : CS, EDS, NA, N&ET, EMT, PE, NNFL, EMS, PSOC,

EM-II LAB, N&ET LAB, PE LAB, EMS LAB

7.4. Post Graduate : **PQ, EDS** 

7.5. Post Graduate Diploma : --

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : 01

b) International

(Journals / Conferences) : --

8.2. Master : **03** 



8.3. Ph. D : --

9. Projects carried out : **05** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : CHAVALI PAVAN KUMAR

2. Date of Birth : **02/07/1989** 

3. Unique ID :

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **5 Years, 5 Months** 

5.1. Teaching : **5 Years, 5 Months** 

5.2. Research : --

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **Power Systems** 

7. Courses taught at :

7.1. Diploma : **BEE, BEE Lab** 

7.2. Post Diploma : --

7.3. Under Graduate : HVE, BEE, SEM, PSOC, EMS, ECA-I, AI Tech, EDS,

HVAC&DC, NNFL, EMF, HVDC, EM-II, BEE Lab, CS Lab,

EMS Lab, PS Lab, N&ET Lab.

7.4. Post Graduate : **PSDS, PS & Simulation Lab** 

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : **01** 

b) International

(Journals / Conferences) : **07** 

8.2. Master : **03** 





8.3. Ph. D :

9. Projects carried out : **08** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : GOLLAMANDALA VENKAT

2. Date of Birth : **06/05/1986** 

3. Unique ID :

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **5 Years** 

5.1. Teaching : **5 Years** 

5.2. Research : --

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **POWER ELECTRONICS** 

7. Courses taught at :

7.1. Diploma : **BEEE, EIE, EC, PE** 

7.2. Post Diploma : --

7.3. Under Graduate : **EC, NA, ET,CS,DCS,EM-I,EC LAB,EM-I LAB,CS LAB,PE LAB** 

7.4. Post Graduate : **PQM, RTCPS** 

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) :

b) International

(Journals / Conferences) : **05** 

8.2. Master :





8.3. Ph. D :

9. Projects carried out : **5** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : **TELU PARDHA SARADHI** 

2. Date of Birth : 16/08/1988

3. Unique ID :

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **5 Years** 

5.1. Teaching : **5 Years** 

5.2. Research : ---

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **POWER ELECTRONICS** 

7. Courses taught at :

7.1. Diploma : **PS-I** 

7.2. Post Diploma : --

7.3. Under Graduate : PE, EMS, PS-I, EDS, HVAC & DC, PEC & D, SGP, BEEE,

EM-II, EM-III, INST, FACTS, DCS, EM-I LAB, EM-II LAB,

PE LAB, N& ET LAB, BEE LAB

7.4. Post Graduate : APSP, PSD

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : 01

b) International

(Journals / Conferences) : 03



8.2. Master : **01** 

8.3. Ph. D : --

9. Projects carried out : --

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : KAMBHAM RAMYA

2. Date of Birth : **17/09/1990** 

3. Unique ID :

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **5 Years** 

5.1. Teaching : **5 Years** 

5.2. Research : ---

5.3. Industry : -

5.4. Others : --

6. Area of Specialization : **ELECTRICAL POWER SYSTEMS** 

7. Courses taught at :

7.1. Diploma : **PS-II, NT, BEEE, NT LAB, EM LAB** 

7.2. Post Diploma : --

7.3. Under Graduate : PS-II, NT, INST, UEE, BEE, ECA-II, PSA, EMS, NT LAB EMS

LAB, EM-II LAB, PE LAB, CS LAB

7.4. Post Graduate :

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : 01

b) International

(Journals / Conferences) : 03

8.2. Master : --





8.3. Ph. D : --

9. Projects carried out : **04** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : MURALI KRISHNA VADAKUPPALA

2. Date of Birth : **03-12-1992** 

3. Unique ID

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **3 YEARS 4 MONTHS** 

5.1. Teaching : **3 YEARS 4 MONTHS** 

5.2. Research : --

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **ELECTRICAL POWER SYSTEMS** 

7. Courses taught at :

7.1. Diploma : --

7.2. Post Diploma : --

7.3. Under Graduate : **RES, CS, BEEE, PS-I, PS-II, EPQ,** 

7.4. Post Graduate : AI TECH

7.5. Post Graduate Diploma : --

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : 02

b) International

(Journals / Conferences) : 11

8.2. Master : --

8.3. Ph. D : --





9. Projects carried out : **02** 

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : MALLURU LAKSHMI SARANYA

2. Date of Birth : **31-07-1990** 

3. Unique ID

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **3 YEARS** 

5.1. Teaching : **3 YEARS** 

5.2. Research : --

5.3. Industry : -

5.4. Others : --

6. Area of Specialization : **POWER ELECTRONICS** 

7. Courses taught at :

7.1. Diploma : --

7.2. Post Diploma : --

7.3. Under Graduate : **EM-I, CS, EMS, CS Lab, N & ET Lab, BEE** 

7.4. Post Graduate : --

7.5. Post Graduate Diploma : --

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : 01

b) International

(Journals / Conferences) : --

8.2. Master : --

8.3. Ph. D : -





9. Projects carried out : --

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details: --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : MIRYALA HARIPRIYA

2. Date of Birth : **21/06/1993** 

3. Unique ID :

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **3 Years** 

5.1. Teaching : **3 Years** 

5.2. Research : --

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **POWER ELECTRONICS** 

7. Courses taught at :

7.1. Diploma : **PE, IE, EEE, EEM, EE LAB, PE LAB, IE LAB WIRING LAB** 

7.2. Post Diploma : --

7.3. Under Graduate : **PS-I, EM-I, EDC, PE LAB, EMS LAB, EM-II Lab, CS Lab** 

7.4. Post Graduate :

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : 01

b) International

(Journals / Conferences) : 01

8.2. Master : --



8.3. Ph. D : --

9. Projects carried out : --

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --



## Byepass Road, Sai Vahini Nagar, Tiruvuru - 521235, Krishna Dt. A.P

### **Department of Electrical & Electronics Engineering**

### A) Profile of each faculty give one page covering with passport size photograph

1. Name of the faculty : **PAMARTHIUDAY KUMAR** 

2. Date of Birth : **25/08/1994** 

3. Unique ID

4. Education qualifications : **B.Tech, M.Tech** 

5. Work experience : **2 Years** 

5.1. Teaching : 2 Years

5.2. Research : ---

5.3. Industry : --

5.4. Others : --

6. Area of Specialization : **POWER SYSTEMS** 

7. Courses taught at :

7.1. Diploma : **BEE, PE LAB** 

7.2. Post Diploma : --

7.3. Under Graduate : **PS-I, PE, EM-I LAB, E. Sim Lab** 

7.4. Post Graduate :

7.5. Post Graduate Diploma :

8. Research guidance :

8.1. No. of papers published in :

a) National

(Journals / Conferences) : 01

b) International

(Journals / Conferences) : --

8.2. Master : --





8.3. Ph. D : --

9. Projects carried out : --

10. Patents : --

11. Technology Transfer : --

12. Research Publications : --

13. No. of books published with details : --